



भारतीय  
प्रौद्योगिकी  
संस्थान  
काशी हिन्दू विश्वविद्यालय



INDIAN  
INSTITUTE OF  
TECHNOLOGY  
BANARAS HINDU UNIVERSITY

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**Recruitment of Junior Research Fellow in the Department of Ceramic Engineering**

Applications are invited from Indian nationals for the following position for the research project of Department of Ceramic Engineering, Indian Institute of Technology (Banaras Hindu University), Varanasi- 221005 for a period of three years.

<b>Project title</b>	Novel Electrode Materials for Reversible Alkali-ion (Li <sup>+</sup> /Na <sup>+</sup> ) capacitors and Pseudocapacitors
<b>Name of the position</b>	Junior Research Fellow*
<b>Number of positions</b>	1
<b>Last date of Application</b>	Hard copy with copy of certificates must reach by <b>24<sup>th</sup> August, 2018</b>
<b>Duration of Position</b>	Three years
<b>Emoluments (p.m.)</b>	Rs. 25,000/- (for first 2 years), 28,000/- (for 3rd Year)
<b>Educational qualification</b>	<b>Essential:</b> M. Tech. In any relevant discipline or M.Sc. in Physics/Chemistry/Materials Sc. /Nanoscience with first class or equivalent CGPA. Candidate with research publications in master level will be given preference. <b>(NET/Gate qualified)</b>
<b>Desirable qualification</b>	<b>Note:</b> Project requires novel materials development on a relatively new research area of Alkali-ion Capacitors. Thus a thorough literature survey, data analysis and structural insight with use of software programming or machine learning will be highly beneficial. Candidate with above mentioned educational experiences will also get preference for the post.
<b>Age:</b>	28 years. Age- relaxable for 5 years for SC/ST/Physically Handicapped/Female candidates as per the norms.
<b>*Note:</b> Candidates interested for PhD have to apply for PhD programme as per the institute norms.	

**General Terms and Conditions:**

1. The position is purely temporary and is coterminous with the project.
2. The committee has the discretion to restrict the number of candidates to be called for interview to a reasonable limit on the basis of qualifications and experience higher than the minimum prescribed in the advertisement. The number of seats may vary at the time of interview.
3. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.
4. The candidate is expected to join immediately, if selected.
5. No TA/DA will be paid for attending the interview.

Application on plain paper giving name, permanent and correspondence address, names of the father and mother, telephone number and e-mail address, details of educational qualifications (starting from high school or equivalent) along with the self-attested copies of all mark-sheets & certificates and details of any research or other experience etc., if any, should reach (**hard copy only**) the Professor In charge at following address by **24<sup>th</sup> August, 2018**.

*hutan Singh (02.08.2018)*

**Dr. Preetam Singh**

Assistant Professor

Dept. of Ceramic Engineering

Indian Institute of Technology (IIT) Varanasi 221005 (U.P.)