Advertisement for the post of Lab Engineer and Project Associate under the SMDP Project of the MeitY, Govt. of India

Applications are invited in the plan paper for the following positions under the SMDP-C2SD project of the MeitY, Govt. of India at the Indian Institute of Technology (BHU), Varanasi.

1.   **Name of the Post:** Lab Engineer

   Number of the posts: 01 (One)

Minimum Qualifications and Experience: Candidate should have first class or equivalent in the M.Tech./M.E. degree in VLSI/Microelectronics/Embedded System Design etc. with a first class or equivalent in the preceding B.Tech./B.E. degree in the Electronics and Communication Engineering or equivalent from a reputed institutions/university. Candidate must have working knowledge in various VLSI design software tools (e.g. Cadence, Synopsys etc.) on both the windows and Linux OS environments. Preference will be given if the candidate has completed successfully a project in the relevant area as a part of his M.Tech./M.E. dissertation work.

Job Responsibilities: The selected candidate should be able to take-up the following tasks:

- To work along with the M.Tech./B.Tech students of the department towards achieving the goals and objectives of the SMDP-C2SD Project
- To maintain a suitable simulation environment for running various VLSI design related software in both the windows and Unix OS based servers
- To prepare technical documentation
- Any other works related to the project to be assigned from time to time.

2. **Name of the Post:** Project Associate

   Number of the posts: 01 (One)

Minimum Qualifications and Experience: Candidate should have first class or equivalent in B.Tech./B.E. degree in the Electronics and Communication Engineering/Computer Engineering or equivalent or MCA from a reputed institutions/university. Candidate must have knowledge in maintaining the servers running on both the windows and Linux OS environments. Preference will be given if the candidate has knowledge in working with VLSI related software.

Job Responsibilities: The selected candidate must be capable of doing the following:

- To maintain/update the servers/workstations/PCs running various VLSI software in the Windows and Unix OS environments.
- To help the undergraduate students in the implementation of their projects in the area of VLSI/SoC
- To prepare various documentation related to the projects
- Any other works related to the project to be assigned from time to time.

General Terms and Conditions:

Duration of the Project: Appointment will be made on purely contractual basis initially for a period of SIX months at Rs. 34,000/ (Rupees Thirty Four Thousand only) per month for the post of Lab Engineer and Rs. 20,000/ (Rupees Twenty Thousand only) for the post of Project Associate. However, the period may be extended later depending on the satisfactory performance of the candidate as well as availability of funds.

Mandatory Information to be Provided in the Application: Name of the Candidate; Father's Name; Mother's Name; Gender; Date of Birth; Contact Details with full address for correspondence, mobile number, email etc.; Details of the Academic Qualifications; Specialization in M.Tech./M.E., if applicable; Title along with a brief write up within 300 words about the M.Tech./M.E. Dissertation is mandatory for the candidates applying for the post of Lab Engineer; Technical skills in the relevant area of VLSI, if any; Work experience, if any; and Any other relevant Information.

Selection Process: Interested candidates of both the posts are required to send their application to **Prof. S. Jit, Chief Investigator, SMDP-C2SD Project, Department of Electronics Engineering, IIT(BHU), Varanasi-221005** by speed post/regular post with a softcopy at sjit.ece@iitbhu.ac.in & skishor.ece@iitbhu.ac.in on or before **June 21, 2019 (Friday)**. The short-listed candidates will be intimated to appear for written test and/or Interview. They are required to bring with all the originals relevant to their academic qualifications/experience etc. No TA/DA and accommodation will be provided for attending the Interview. The institute reserves the right to fill or not to fill any of the above posts without any justification.